

| L Number | Hits  | S arch Text   | DB  | Time stamp                           |
|----------|-------|---|---|--------------------------------------|
| 1        | 453   | ((feedback or feed-back or (fed adj back)) same<br>(((diode same capacit\$5) same resist\$5) same<br>series))   | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT               | 2002/09/19 10:13                     |
| 6        | 3008  | ((feedback or feed-back or (fed adj back)) same<br>(((diode same capacit\$5) same resist\$5) same<br>series))   | USPAT   | 2002/09/19 10:14                     |
| 12       | 2401  | ((feedback or feed-back or (fed adj back)) same<br>(((diode same capacit\$5) same resist\$5) same<br>series))) and (amplifier or amplifying)                      | USPAT   | 2002/09/19 10:26                     |
| 7        | 199   | ((feedback or feed-back or (fed adj back)) same<br>(((diode same capacit\$5) same resist\$5) same<br>series))) and (amplifier or amplifying)                      | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT               | 2002/09/19 10:48                     |
| 13       | 208   | ((feedback or feed-back or (fed adj back)) same<br>(((diode same capacit\$5) same resist\$5) same<br>series))) and (amplifier or amplifying)) and<br>330/\$.ccls. | USPAT   | 2002/09/19 10:26                     |
| -        | 382   | (330/110 or 330/86).ccls. and (feedback or<br>feed-back)  | USPAT   | 2002/09/06 14:21                     |
| -        | 376   | ((330/110 or 330/86).ccls. and (feedback or<br>feed-back)) and @ad<=20001127  | USPAT   | 2002/09/06 13:50                     |
| -        | 403   | 330/\$.ccls. and (henry near1 choe)   | USPAT<br>USPAT;<br>US-PGPUB;                            | 2002/09/06 13:51<br>2002/09/06 15:03 |
| -        | 18332 | (feedback or feed-back) and ((diode same<br>capacitor) same resistor) and series  | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT;<br>US-PGPUB; | 2002/09/06 14:24                     |
| -        | 14099 | ((feedback or feed-back) and ((diode same<br>capacitor) same resistor) and series) and amplifier  | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT;<br>US-PGPUB; | 2002/09/06 14:24                     |
| -        | 1029  | ((feedback or feed-back) and ((diode same<br>capacitor) same resistor) and series) and amplifier)<br>and 330/\$.ccls.   | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT;<br>US-PGPUB; | 2002/09/06 14:24                     |
| -        | 224   | ((feedback or feed-back) and ((diode same<br>capacitor) same resistor) and series) and amplifier  | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT;<br>US-PGPUB; | 2002/09/06 14:24                     |
| -        | 129   | ((feedback or feed-back) and ((diode same<br>capacitor) same resistor) and series) and amplifier)<br>and @ad<=20001127  | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT;<br>US-PGPUB; | 2002/09/06 14:25                     |
| -        | 1007  | ((feedback or feed-back) and ((diode same<br>capacitor) same resistor) and series) and amplifier)<br>and 330/\$.ccls.) and @ad<=20001127                          | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT;<br>US-PGPUB; | 2002/09/06 14:28                     |
| -        | 360   | ((negative adj (feedback or feed-back)) same<br>(((diode and capacitor) same resistor) same<br>series))   | EPO; JPO;<br>DERWENT;<br>IBM_TDB<br>USPAT;<br>US-PGPUB; | 2002/09/19 10:10                     |

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**1 A new method of frequency compensation for bipolar Wilson current mirror***Filanovsky, I.M.; Lee, W.H.Y.*

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on , Volume: 46 May 1999

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**2 Instantaneous junction temperature evaluation of high-power diodes (thyristors) during c transients***Profumo, F.; Tenconi, A.; Facelli, S.; Passerini, B.*

Power Electronics, IEEE Transactions on , Volume: 14 Issue: 2 , March 1999

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**3 InP-based high sensitivity pin/HEMT/HBT monolithic integrated optoelectronic receiver**  
*Kiziloglu, K.; Yung, M.W.; Hsiang-Chi Sun; Thomas, S., III; Kardos, M.B.; Walden, R.H.; Brown Stanchina, W.E.; Kaman, V.; Rodwell, M.J.W.*

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**4 A 30 V complementary bipolar technology on SOI for high speed precision analog circuits***Patel, R.; Milan, W.; Cooley, G.; Corsi, M.; Erdeljic, J.; Hutter, L.*

Bipolar/BiCMOS Circuits and Technology Meeting, 1997. Proceedings of the , 1997

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**5 State v ct r equation based computer simulation appr ach f digitally controlled power c  
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*Ulhaq, S.M.; Nakaoka, M.; Takano, H.*

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